

### **REMARKS**

Claims 1-11 are now present in this application.

Claims 1 and 4 have been amended, and claims 8-11 have been presented.

Reconsideration of the application, as amended, is respectfully requested.

#### **Objection to the Drawings**

The Examiner has indicated in the Office Action Summary that the drawings are objected to. Although the Office Action Summary indicates that the objection to the drawings cannot be held in abeyance, it is respectfully noted that the Detailed Action contains no specific comments regarding the drawings. Accordingly, any objection to the drawings is unclear, and it would therefore be impossible for the Applicant to respond to such a rejection at this point in time. It is respectfully requested that the Examiner clarify his objection to the drawings in the next action.

#### **Amendments to the Claims**

Claim 1 has been amended to include the limitation of the frame data corresponds to a frame of a display screen without OSD data, in order to emphasize that the frame data and the OSD data are different before copying the OSD data to the registers corresponding to the OSD window. Also, the phrase "copying the OSD data in the register corresponding to the OSD window" as been changed to --copying the OSD data and storing the OSD data to the registers corresponding to the OSD window--. Support for these amendments can be found in Fig. 2 and on page 6, lines 3-5, which state that "in step S26, the OSD data, which corresponds to the OSD window, is copied into the registers of a display buffer."

Claim 4 has been amended to better correspond to amended claim 1.

Claim 8 has been added. Support for this claim can be found in Fig. 2 and page 6, lines 3-5 of the specification, which states that “in step S26, the OSD data, which corresponds to the OSD window, is copied into the registers of a display buffer.” It is therefore clear that the data stored in the display buffer comprises the frame data and the OSD data after storing the OSD data to the registers corresponding to the OSD window.

Claims 9 and 10 have been added. Support for these claims can be found in FIGs. 2 and 3, wherein the data output to the display device (including 50 and 60) comprises outputting the data comprising the frame data and the OSD data stored in the display buffer to a display device frame data and the OSD data stored in the display buffer 20.

Claim 11 has been added. Support for this claim can be found in Fig. 2 and page 6, lines 3-5 of the specification, which states that, “in step S26, the OSD data, which corresponds to the OSD window, is copied into the registers of a display buffer.”

Because support for all amendments to the claims can be found in the originally filed specification and drawings, it is respectfully submitted that no new matter is present.

*Rejections under 35 USC 102(e) and 103*

Claims 1-3 stand rejected under 35 USC 102(e) as being anticipated by Cottle et al., U.S. Patent 6,263,396. This rejection is respectfully traversed.

Claims 4-7 stand rejected under 35 USC 103 as being unpatentable over Cottle et al. This rejection is respectfully traversed.

Independent claim 1 recites an OSD control method comprising storing frame data, which corresponds to a frame of a display screen without OSD data, in a display buffer with plural registers; storing the OSD data, which corresponds to an OSD window included in the frame, in a memory; and copying the OSD data and storing the OSD data to the registers corresponding to the OSD window by a data processing method.

The patent to Cottle et al. does not teach or suggest the step of storing frame data, which corresponds to a frame of a display screen without OSD data, in a display buffer with plural registers.

Cottle et al. recites that the display area that does not contain any OSD data, such as background color or motion video, is not included in the frame memory (see column 42, lines 37-39, and FIG. 18E). Thus, data stored in the frame memory of Cottle et al. is OSD data only, not “the frame data corresponding to a frame of a display screen without OSD data”.

It is therefore respectfully submitted that the prior art utilized by the Examiner fails to teach or suggest the method of independent claim 1 and its dependent claims.

With regard to the newly presented claim 8, this claim recites, in part, “the display buffer stores the frame data and the OSD data after storing the OSD data to the registers corresponding to the OSD window”. It is respectfully submitted that this limitation is not taught by the prior art utilized by the Examiner.

With regard to the newly presented claims 9 and 10, these claims recite, in part, “outputting the data comprising the frame data and the OSD data stored in the display buffer to a display device”. It is respectfully submitted that this limitation is not taught by the prior art utilized by the Examiner.

With regard to newly presented claim 11, this claim recites, in part, "storing another OSD data after storing the OSD data to the registers". It is respectfully submitted that this limitation is not taught by the prior art utilized by the Examiner.

Accordingly, it is respectfully submitted that independent claim 1, as well as its dependent claims 2-11, are neither taught nor suggested by the prior art utilized by the Examiner. Accordingly, reconsideration and withdrawal of the 35 USC 102(e) and 103 rejections are respectfully requested.

Conclusion

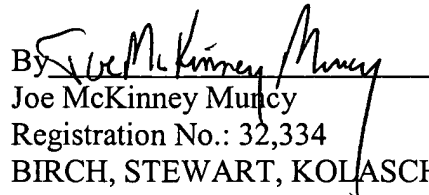
Favorable reconsideration and an early Notice of Allowance are earnestly solicited.

In the event that any outstanding matters remain in this application, the Examiner is invited to contact the undersigned at (703) 205-8000 in the Washington, D.C. area.

If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 02-2448 for any additional fees required under 37 C.F.R. §§ 1.16 or 1.17; particularly, extension of time fees.

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Respectfully submitted,

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